

- PRESS RELEASE -

Major New Functionality for SI/PI Engineers in CST PCB STUDIO 2012

Santa Clara, CA, January 31, 2012, Computer Simulation Technology (CST) will be previewing the new release of CST PCB STUDIO 2012 at DesignCon 2012, booth #403.

Signal and Power Integrity (SI/PI) engineers working on the design of power distribution systems will benefit from the latest additions to CST PCB STUDIO™ (CST PCBS), the CST STUDIO SUITE® tool for fast post-layout analysis.

CST PCB STUDIO IR-Drop Solver

The new CST PCBS IR-drop solver allows the quick calculation of the current distribution and voltage drop on multilayered PCBs and packages. DC analysis for off-chip power delivery systems is mandatory for high-current, low-voltage designs and the new IR-drop simulation helps SI/PI engineers to perform the adjustment of Voltage Regulator Modules (VRM) nominal output, strategic placement of lines and the early identification of problematic potential distributions. Budgeting for AC noise and system-level IR drop enables the optimization of voltage margins for every device of the PCB.

CST PCB STUDIO PI Solver

The CST PCBS PI solver, used to verify target impedance requirements, also presents a number of enhancements such as models with increased accuracy for vias and seamless decaps placement on imported PCB/package layouts. The new 3D impedance distribution plot helps engineers to visually identify problematic areas, and to study variations of the original layout.

“The EDA market’s increasing importance to CST is reflected by our major product enhancements in this area. The 2012 version of CST PCB STUDIO makes a significant contribution to the EDA design flow,” commented Dr. Peter Thoma, Managing Director R&D, CST. *“The new IR-drop solver and the numerous CST PCB STUDIO enhancements enable SI/PI engineers to verify power distribution systems efficiently and provide an excellent design environment for signal and power integrity analysis.”*

About CST

CST develops and markets high performance software for the simulation of electromagnetic fields in all frequency bands. Its success is based on the implementation of leading edge technology in a user-friendly interface. CST's customers are market leaders in industries as diverse as Telecommunications, Defense, Automotive, Electronics, and Medical Equipment. Today CST employs 190 sales, development, and support personnel, and enjoys a market share of over 30% in high frequency 3D EM simulation.

CST STUDIO SUITE is the culmination of many years of research and development into the most accurate and efficient computational solutions for electromagnetic designs. It comprises CST's tools for the design and optimization of devices operating in a wide range of frequencies - static to optical. Analyses may include thermal and mechanical effects, as well as circuit simulation. CST STUDIO SUITE benefits from an integrated design environment which gives access to the entire range of solver technology. System assembly and modeling facilitates multi-physics and co-simulation as well as the management of entire electromagnetic systems. CST STUDIO SUITE can offer considerable product to market advantages such as shorter development cycles, virtual prototyping before physical trials, and optimization instead of experimentation.

Further information about CST is available on the web at <http://www.cst.com>.

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This screenshot illustrates: "IR-drop : surface current density distribution on multilayer PCB"